■片式排容CA系列0603×4
C-ARRAY SERIES 0603×4

• 优点：

* 节约空间：可以节省高达50%的PCB空间位置，提高装配密度
* 更高的体积比容：安装一块CA等于安装4 块0603片容，减少安装次数，提高安装效率。
* 降低成本：减少放置的次数；缩短生产时间；减少设备管理费用；减少PCB费用。
* 安装简易：可进行SMT编带包装，由贴片机高速贴片。
* 提高线路板工作效率：可以减少印刷的线路，提高线路板的运转速度，提高工作效率。

• FEATURE：

* Space saving: CA can save 50% space of the PC board and improve the assembling density.
* Provide more capacitance per volumetric area: Efficiently use the side margins and thickness. Promoting mounting efficiency.One chip of CA equals to four chips of 0603 type capacitor. So it can reduce times of picking and placing.
* Cost saving: Reduce times for picking and placing, reduce manufacturing time, reduce the cost for manage the equipments and reduce the cost of PCB.
* Easy to picking and placing: SMT package, easy to mounting.
* Improve the working efficiency of the printed board: Reduce the amount of printed circuits and promote
* the working speed of the printed circuit.

• 用途：

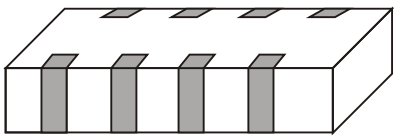
* 适用于对元器件空间要求严格的PCB，如手提电脑、PDA、无绳电话
* 特别适用于输入、输出接口电路

• APPLICATIONS：

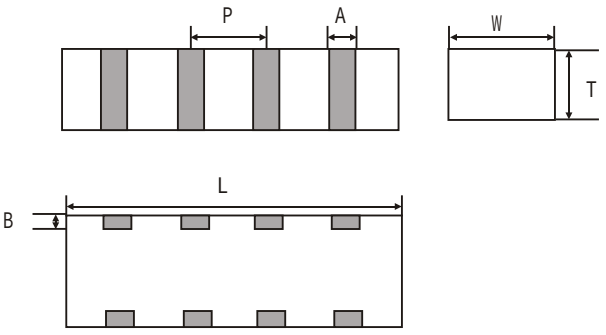
* Applied in PCB which require strictly about space speed,such as notebook computer, PDA and portable telephone,etc.
* CA is best suitable to use in I/O interface circuit.

• 结构及尺寸 STRUCTURE AND DIMENSIONS

* 结构 STRUCTURE

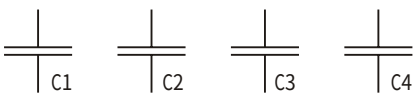


排容外形图(Outlook Figure)



排容三视图(Tri-side outlook)

等效电路图(Equivalent Circuit)：



C1、C2、C3、C4标称容量相等。
The nominal capacitance of C1,C2,C3,C4 equals to each other.

＊ 尺寸 DIMENSIONS

单位(unit):mm(inch)

类型 Type	单位Unit	长L	宽W	厚T	A	B	P
0603 ×4	mm	3.20±0.15	1.60±0.15	0.80±0.10 1.00±0.10	0.45±0.10	0.20±0.10	0.08±0.10
	inch	0.126±0.006	0.063±0.006	0.031±0.004 0.039±0.004	0.018±0.004	0.008±0.004	0.032±0.004

• 订货方式 HOW TO ORDER

CA | A | 4 | B | 103 | K | 500 | N | T | 1

CA系列 片式 排容 C-ARRAY

表示4个内置 独立单元 Including 4 discrete chips

单元尺寸 Dimensions	
A	0603
B	0502

介质 Dielectric	
CG	COG(NPO)
B	X7R
F	Y5V

容量 Capacitance	
103	10×10³PF
100	10×10⁰PF

表示容量偏差范围 Capacitance Tolerance	
J	±5%
K	±10%
M	±20%

额定电压 Rated Voltage	
160	16×10⁰V
250	25×10⁰V
500	50×10⁰V

端头材料 Termination Material	
S	纯银Sliver
N	银/镍/锡三层 Sliver/Nickel/Tin Three layers

包装形式 Package style	
T	编带Tape & Real
N	散包装Bulk

厚度(mm)	
1	0.8
2	1.0

• 电性能 ELECTRICAL PROPERTIES

项 目 ITME	0603×4	NPO/COG	X7R	Y5V
使用温度范围 Operating Temperature		-55°C~+125°C	-55°C~+125°C	-25°C~+85°C
容量范围 Capacitance Range		1pF~1000PF	470pF~27nF	10nF~220nF
容量偏差范围 Capacitance Tolerance		±5%; ±10%;	±20%; ±10%	±20; +80%/-20%
额定电压 Rated Voltage		50V/25V/16V	50V/25V/16V	50V/25V/16V
绝缘电阻 Insulation Resistance		R>10¹¹Ω	C<10nF, R>10¹¹Ω C≥10nf, RC≥1000S	RC>500S
介质损耗 Dissipation Factor		≤0.15%	≤2.5%	≤5%(25V, 50V) ≤7%(16V)
耐电压 Dielectric Withstanding Voltage		>2.5×Vr	>2.5×Vr	>2.5×Vr
温度特性 Temperature coefficient		(0±30)PPM/°C	±15%	+30% -80%
端电极类型 Termination		Ag或Ag/Ni/Sn	Ag或Ag/Ni/Sn	Ag或Ag/Ni/Sn
注解：测量条件：NPO: 1MHz/1V; X7R: 1KHz/1V; Y5V: 1KHz/0.5V Note: Test conditions: NPO: 1MHz/1V; X7R: 1KHz/1V; Y5V: 1KHz/0.5V				